DUAL 4-INPUT POSITIVE-NAND GATES WITH OPEN-COLLECTOR OUTPUTS
DECEMBER 1983 — REVISED MARCH 1988

- Package Options Include Plastic "Small Outline" Packages, Ceramic Chip Carriers and Flat Packages, and Plastic and Ceramic DIPs
- Dependable Texas Instruments Quality and Reliability

#### description

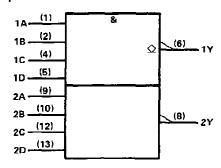
These devices contain two independent 4-input NAND gates. The open-collector outputs require pull-up resistors to perform correctly. They may be connected to other open-collector outputs to implement active-low wired-OR or active-high wired-AND functions. Open-collector devices are often used to generate higher VOH levels.

The SN5422, SN54LS22 and SN54S22 are characterized for operation over the full military temperature range of -55°C to 125°C. The SN7422, SN74LS22, and SN74S22 are characterized for operation from 0°C to 70°C.

#### FUNCTION TABLE (each gate)

	INP	uts		OUTPUT
A	В	С	D	Y
Н	Н	Н	н	L
L	X	X	x	H
Х	L	х	х	Н
х	X	L	×	н
х	x	х	L	н

#### logic symbol<sup>†</sup>



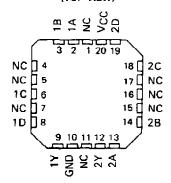
<sup>&</sup>lt;sup>†</sup> This symbol is in accordance with ANSI/IEEE Std 91-1984 and IEC Publication 617-12.

Pin numbers shown are for D, J, N, and W packages.

SN5422, SN54LS22, SN54S22 . . . J OR W PACKAGE SN7422 . . . N PACKAGE SN74LS22, SN74S22 . . . D OR N PACKAGE (TOP VIEW)

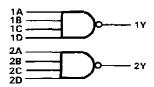
1A [	<b>]</b> 1	U14□ Vcc
1B [	2	13 🗀 2 D
NC (	3	12 2 C
1C [	<b>]</b> 4	11□ NC
1D [	<b>1</b> 5	10 2B
1Y [	6	9 🗀 2A
GND [	7	8 🗍 2Y

\$N54L\$22, \$N54\$22 . . . FK PACKAGE (TOP VIEW)



NC-No internal connection

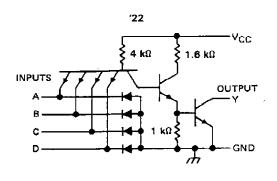
#### logic diagram

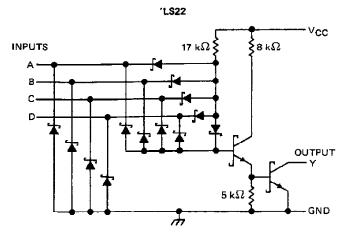


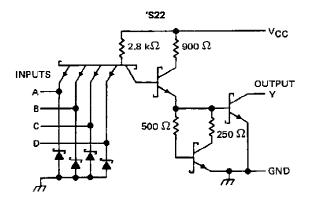
#### positive logic

 $Y = \overline{A \cdot B \cdot C \cdot D}$  or  $Y \approx \overline{A} + \overline{B} + \overline{C} + \overline{D}$ 

#### schematics (each gate)







Resistor values shown are nominal.

#### absolute maximum ratings over operating free-air temperature range (unless otherwise noted)

Supply voltage, VCC (See Note 1)		
Input voltage: '22, 'S22		5.5 V
	,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,	
Operating free-air temperature range:	SN54'	- 55°C to 125°C
	SN74'	0° C to 70°C
Storage temperature range		-65°C to 150°C

NOTE 1: Voltage values are with respect to network ground terminal.



#### recommended operating conditions

•			SN5422			SN7422		
		MIN	NOM	MAX	MIN	NOM	MAX	UNIT
V <sub>CC</sub> s	upply voltage	4.5	5	5.5	4.75	5	5.25	٧
V <sub>IH</sub> H	ligh-level input voltage	2			2			V
VIL L	.ow-level input voltage			8,0			0.8	٧
Von H	ligh-level output voltage			5,5			5.5	٧
OL L	ow-level output current			16			16	mA
T <sub>A</sub> C	perating free-air temperature	- 55		125	0		70	°C

## electrical characteristics over recommended operating free-air temperature range (unless otherwise noted)

242445752	TEST CONDITIONS †	SN5422	SN7422	UNIT
PARAMETER	LEST COMPLITORS	MIN TYP <sup>‡</sup> MAX	MIN TYP <sup>‡</sup> MAX	UNII
ViK	$V_{CC} = MIN$ , $I_{I} = -12 \text{ mA}$	-1.5	- 1.5	٧
loн	VCC = MIN, VIL = 0.8 V, VOH = 5.5 V		0.25	mA
	$V_{CC} = MIN$ , $V_{IL} = 0.7 \text{ V}$ , $V_{OH} = 5.5 \text{ V}$	0.25		MA
VOL	V <sub>CC</sub> = MIN, V <sub>IH</sub> = 2 V, I <sub>OL</sub> = 16 mA	0.2 0.4	0.2 0.4	V
ŧ <sub>I</sub>	V <sub>CC</sub> = MAX, V <sub>I</sub> = 5.5 V	1	1	mA
<sup>I</sup> IH	V <sub>CC</sub> = MAX, V <sub>I</sub> = 2.4 V	40	40	μΑ
կը	V <sub>CC</sub> = MAX, V <sub>I</sub> = 0.4 V	-1.6	-1.6	mΑ
Іссн	$V_{CC} = MAX, V_I = 0$	2 4	2 4	mA
<sup>I</sup> CCL	V <sub>CC</sub> = MAX, V <sub>I</sub> = 4.5 V	6 11	6 11	mA

TFor conditions shown as MIN or MAX, use the appropriate value specified under recommended operating conditions.

#### switching characteristics, $V_{CC} = 5 \text{ V}$ , $T_A = 25^{\circ}\text{C}$ (see note 2)

PARAMETER	FROM ((NPUT)	TO (OUTPUT)	TEST CONDITIONS	MIN TYP	MAX	UNIT
<sup>t</sup> PLH	Any	v	$R_L = 4 k \Omega$ , $C_L = 15 pF$	35	45	កទ
<sup>†</sup> PHL	City	·	$R_L = 400 \Omega$ , $C_L = 15 pF$	8	15	ns

NOTE 2: Load circuits and voltage waveforms are shown in Section 1.

<sup>&</sup>lt;sup>‡</sup>All typical values are at  $V_{CC} = 5 \text{ V}$ ,  $T_A = 25 \text{ °C}$ .

# SN54LS22, SN74LS22 DUAL 4-INPUT POSITIVE-NAND GATES WITH OPEN-COLLECTOR OUTPUTS

#### recommended operating conditions

	SN54LS22				SN74LS22			
	MIN	NOM	MAX	MIN	NOM	MAX	UNIT	
V <sub>CC</sub> Supply voltage	4.5	5	5.5	4.75	5	5.25	V	
VIH High-level input voltage	2	-		2			٧	
V <sub>IL</sub> Low-level input voltage	,		0.7			0.8	٧	
VOH High-level output voltage			5. <b>5</b>			5.5	٧	
IOL Low-level output current			4		•	8	mΑ	
TA Operating free-air temperature	<b>– 55</b>	-	125	0		70	ိင	

#### electrical characteristics over recommended operating free-air temperature range (unless otherwise noted)

		TEST CONDITIONS †			N54LS	22	SN74 LS22			UNIT
PARAMETER		LEST CONDI	TEST CONDITIONS T		TYP‡	MAX	MIN	TYP‡	MAX	UNII
VIK	V <sub>CC</sub> = MIN,	I <sub>I</sub> = — 18 mA				- 1.5			- 1.5	٧
¹он	V <sub>CC</sub> = MIN,	VIL = MAX,	V <sub>OH</sub> = 5.5 V			0.1			0.1	mA
\/	V <sub>CC</sub> = MIN,	V <sub>IH</sub> = 2 V,	IOL = 4 mA		0.25	0.4		0.25 0.4 V	, ,	
VOL	V <sub>CC</sub> = MIN,	V <sub>IH</sub> = 2 V,	I <sub>OL</sub> = 8 mA					0.35	0.5	1
T <sub>1</sub>	V <sub>CC</sub> = MAX,	V <sub>I</sub> = 7 V				0.1			0.1	mΑ
IН	VCC = MAX.	V <sub>1</sub> = 2.7 V				20			20	μА
IL	V <sub>CC</sub> = MAX,	V <sub>1</sub> = 0.4 V				- 0.4			- 0.4	mΑ
Гссн	V <sub>CC</sub> = MAX,	V <sub>1</sub> = 0		·	0.4	8.0		0.4	8.0	mA
<sup>1</sup> CCL	V <sub>CC</sub> = MAX,	V <sub>1</sub> = 4.5 V			1.2	2.2		1.2	2.2	mA

<sup>†</sup> For conditions shown as MIN or MAX, use the appropriate value specified under recommended operating conditions.

#### switching characteristics, VCC = 5 V, TA = 25°C (see note 2)

PARAMETER	FROM (INPUT)	TO (OUTPUT)	TEST CONDITIONS	MIN TYP	MAX	UNIT
tpLH	Αηγ	Y	$R_{\parallel} = 2 k \Omega$ , $C_{\parallel} = 15 pF$	17	32	ns
ФНГ	, , ,	•	,,,, 2 may, 3 mg, 10 p.	15	28	ns

NOTE 2: Load circuits and voltage waveforms are shown in Section 1.

<sup>‡</sup> All typical values are at  $V_{CC}$  = 5 V,  $T_A$  = 25°C.

#### recommended operating conditions

			SN54S22			SN74S22		
		MIN	NOM	MAX	MIN	NOM	MAX	UNIT
V <sub>CC</sub> S	Supply voltage	4.5	5	5.5	4.75	5	5.25	٧
VIH F	digh-level input voltage	2			2			V
VIL L	ow-level input voltage			8.0			8,0	V
V <sub>OH</sub> F	ligh-level output voltage			5.5			5.5	٧
IOL L	ow-level output current			20			20	mA
T <sub>A</sub> C	Operating free-air temperature	- 55		125	0	•	70	°C

#### electrical characteristics over recommended operating free-air temperature range (unless otherwise noted)

	TEST CONDITIONS†	SN54S22	SN74S22	UNIT
PARAMETER	TEST CONDITIONS.	MIN TYP <sup>‡</sup> MAX	MIN TYP‡ MAX	UNIT
ViK	V <sub>CC</sub> = MIN, I <sub>I</sub> = -18 mA	-1.2	-1.2	>
	V <sub>CC</sub> = MIN, V <sub>IL</sub> = 0.8 V, V <sub>OH</sub> = 5.5 V		0.25	mΑ
ЮН	V <sub>CC</sub> = MIN, V <sub>IL</sub> = 0.7 V, V <sub>OH</sub> = 5.5 V	0.25		11124
VOL	V <sub>CC</sub> = MIN, V <sub>IH</sub> = 2 V, I <sub>QL</sub> = 20 mA	0.5	0.5	٧
h .	V <sub>CC</sub> = MAX, V <sub>I</sub> = 5.5 V	1	1	mA
<sup>ј</sup> ін	VCC = MAX, V <sub>1</sub> = 2.7 V	50	50	μΑ
lir.	V <sub>CC</sub> = MAX, V <sub>I</sub> = 0.5 V	-2	- 2	mA
<sup>1</sup> ССН	$V_{CC} = MAX$ , $V_I = 0$	3 6.6	3 6.6	mΑ
<sup>1</sup> CCL	$V_{CC} = MAX$ , $V_{\parallel} = 4.5 \text{ V}$	10 18	10 18	mA

 $<sup>^{\</sup>dagger}$ For conditions shown as MIN or MAX, use the appropriate value specified under recommended operating conditions.  $^{\ddagger}$ All typical values are at V<sub>CC</sub> = 5 V, T<sub>A</sub> = 25 °C.

#### switching characteristics, VCC = 5 V, TA = 25°C (see note 2)

PARAMETER	FROM (INPUT)	TO (OUTPUT)	TEST CON	MIN	TYP	MAX	UNIT	
t <b>P</b> LH			D 000 0	C 15.5	2	5	7.5	nş
t <sub>PHL</sub>	Any		R <sub>L</sub> = 280 Ω,	C <sub>L</sub> ~ 15 pF	2	4.5	7	ns
<sup>t</sup> PLH	אייץ			C <sub>L,</sub> = 50 pF		7.5		ns
<sup>t</sup> PH L			R <sub>L</sub> = 280 Ω,			7		ns

NOTE 2: Load circuits and voltage waveforms are shown in Section 1.

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